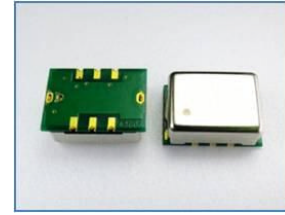


LVDS VCXO

VXSG-Y9S6 Series

◆特長・用途/Feature

- ・SMD Package
- ・Supply Voltage **+3.3V**
- ・Output Level **LVDS**
- ・Frequency range **10MHz ~ 150MHz**

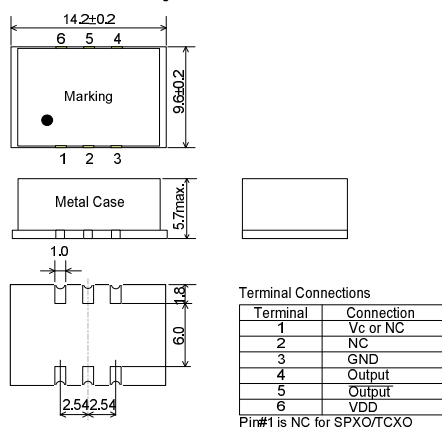


◆仕様/Specifications

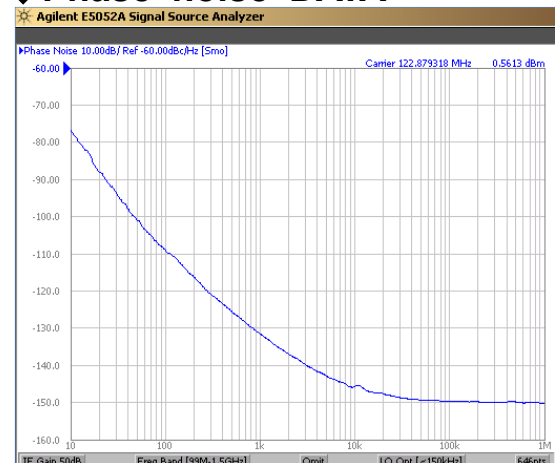
RoHS Compliant

項目 / item	Min	Max	単位 / unit	Note
出力周波数 Frequency range	10	150	MHz	Standard Frequency 122.88 MHz
動作温度範囲 Operating Temp. range	-40	+85	°C	--
電源電圧 Power Supply voltage	+3.3		V	--
消費電流 Current	--	30	mA	--
周波数安定度 Freq. Overall tolerance	±45		10 ⁻⁶	Inclusive of initial tolerance , operating temperature , supply voltage change and load change.
周波数可変範囲 Freq. control range	±75	±150	10 ⁻⁶	Control Voltage (VC) : +0V~+3.3V Input impedance : 100kΩ min.
出力レベル Output level	LVDS LEVEL			
	0.247 ≤ Vod ≤ 0.454		V	test circuit 1
	0.494 ≤ Vod p-p ≤ 0.908			
	1.125 ≤ Voc ≤ 1.375		V	test circuit 2
Rise time , Fall time	--	1	ns	20% ⇄ 80%(test circuit 1)
Duty Cycle	45	55	%	test circuit 1
位相雑音 Phase noise (例 : 122.88MHz)	--	-55	dBc/Hz	10Hz Offset
	--	-92	dBc/Hz	100Hz Offset
	--	-122	dBc/Hz	1kHz Offset
	--	-140	dBc/Hz	10kHz Offset
	--	-145	dBc/Hz	100kHz Offset

◆外形寸法/Dimensions unit : mm

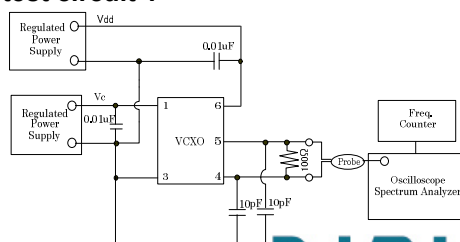


◆Phase noise DATA



◆測定回路/Test circuit

test circuit 1



test circuit 2

